



RF detector daughterboard

Board stack up

|                                |                      |  |            |
|--------------------------------|----------------------|--|------------|
| ▼ Material                     |                      | Buildup                                      |            |
| Board thickness                | 1.55 mm              | Material Tg                                  | 145-150 °C |
| Outer layer copper foil        | 18 µm (end +/-35 µm) |  |            |
| ▼ Technology                   |                      | Classification                               |            |
| Outer layer trackwidth (OL-TW) | 0.150 mm             | Outer layer isolation distance (OL-TT-TP-PP) | 0.150 mm   |
| Outer layer annular ring (OAR) | 0.125 mm             | Smallest final hole                          | 0.25 mm    |
| Hole density                   | < 1000/dm²           | Technology class                             | 6C         |
| Holes <= may be reduced        | 0.45 mm              |  |            |

|   |                                |  |  |
|---|--------------------------------|--|--|
| Title: <b>Digital Filter Wall Board</b><br><b>DFW v1.0 - [01] RF daughter</b> |                                | Description:<br><b>Release Candidate</b> |  |
| Assembly variant:<br><b>var: 01</b>   |                                | Pages:<br><b>01/01</b>                   |  |
| Manufacturer:<br><b>ATOMKI</b><br>18/c. Bem Square, Debrecen, H-4026          |                                | Revision by:<br><b>Dr. József Molnár</b> |  |
| Date:<br><b>2017.06.13.</b>   | Drawn by:<br><b>Csaba Dósa</b> | Revision:<br><b>Rev2</b>                 |  |

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